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\* Defines for 16 bit timers used with Servo library

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\* If \_useTimerX is defined then TimerX is a 16 bit timer on the current board

\* timer16\_Sequence\_t enumerates the sequence that the timers should be allocated

\* \_Nbr\_16timers indicates how many 16 bit timers are available.

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\* SAM Only definitions

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// For SAM3X:

#define \_useTimer1

#define \_useTimer2

#define \_useTimer3

#define \_useTimer4

#define \_useTimer5

/\*

TC0, chan 0 => TC0\_Handler

TC0, chan 1 => TC1\_Handler

TC0, chan 2 => TC2\_Handler

TC1, chan 0 => TC3\_Handler

TC1, chan 1 => TC4\_Handler

TC1, chan 2 => TC5\_Handler

TC2, chan 0 => TC6\_Handler

TC2, chan 1 => TC7\_Handler

TC2, chan 2 => TC8\_Handler

\*/

#if defined (\_useTimer1)

#define TC\_FOR\_TIMER1 TC1

#define CHANNEL\_FOR\_TIMER1 0

#define ID\_TC\_FOR\_TIMER1 ID\_TC3

#define IRQn\_FOR\_TIMER1 TC3\_IRQn

#define HANDLER\_FOR\_TIMER1 TC3\_Handler

#endif

#if defined (\_useTimer2)

#define TC\_FOR\_TIMER2 TC1

#define CHANNEL\_FOR\_TIMER2 1

#define ID\_TC\_FOR\_TIMER2 ID\_TC4

#define IRQn\_FOR\_TIMER2 TC4\_IRQn

#define HANDLER\_FOR\_TIMER2 TC4\_Handler

#endif

#if defined (\_useTimer3)

#define TC\_FOR\_TIMER3 TC1

#define CHANNEL\_FOR\_TIMER3 2

#define ID\_TC\_FOR\_TIMER3 ID\_TC5

#define IRQn\_FOR\_TIMER3 TC5\_IRQn

#define HANDLER\_FOR\_TIMER3 TC5\_Handler

#endif

#if defined (\_useTimer4)

#define TC\_FOR\_TIMER4 TC0

#define CHANNEL\_FOR\_TIMER4 2

#define ID\_TC\_FOR\_TIMER4 ID\_TC2

#define IRQn\_FOR\_TIMER4 TC2\_IRQn

#define HANDLER\_FOR\_TIMER4 TC2\_Handler

#endif

#if defined (\_useTimer5)

#define TC\_FOR\_TIMER5 TC0

#define CHANNEL\_FOR\_TIMER5 0

#define ID\_TC\_FOR\_TIMER5 ID\_TC0

#define IRQn\_FOR\_TIMER5 TC0\_IRQn

#define HANDLER\_FOR\_TIMER5 TC0\_Handler

#endif

typedef enum { \_timer1, \_timer2, \_timer3, \_timer4, \_timer5, \_Nbr\_16timers } timer16\_Sequence\_t ;